An Active High-Voltage Divider and Phase Shifter

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Abstract—An instrument combining the functions of an active high voltage divider and a phase shifter designed to yield small phase-angle uncertainties within 20 μrad is described. It is based on a circuit employing a feedback amplifier and a controlled source. The presence of the controlled source greatly reduces the potential for dynamic instabilities of the feedback loop and effectively eliminates the errors associated with the finite gain of the amplifier. Design and construction details and the results of the accuracy evaluation are presented.

I. INTRODUCTION

IN tests to determine power losses of large inductive devices such as power transformers and high-voltage inductors (shunt reactors), measurement instruments (power transducers, voltage and current transformers) are required having very low phase-angle uncertainty. Depending upon the power factor of the load that is measured, the phase-angle uncertainty of the measurement system must be maintained in the 10–100-μrad range [1], [2].

A commonly used configuration of equipment for measuring power losses consists of three principal parts: 1) an electronic power transducer; 2) an active voltage divider; and 3) a current transformer compensated either electronically or through two-stage construction. To obtain a number of power, voltage, and current ranges, each principal part is usually combined with precision amplifiers. The general complexity of the resulting test system mandates means for verifying its accuracy. Highly accurate standard wattmeters, voltage dividers, and current transformers have been previously developed and described [3]–[7]. In measurements at low power factors where the real power must be measured with an uncertainty of about one percent (but the uncertainty in the real power must be as low as several tens of parts per million when expressed in terms of the apparent power), only the phase-angle uncertainty is critical in the measurement system, thus reducing the requirements on the measuring instruments and calibrating standards.

This paper describes an instrument that can be used as an accurate high-voltage divider or a phase shifter. It was specifically designed to calibrate test systems that measure load losses of power transformers. Since the instrument is intended for applications involving low power factors, the accuracy emphasis is on the phase angle. The instrument is based on a compensated operational amplifier. The compensation scheme involves a controlled source and is particularly attractive for high-voltage divider applications, since compatible high-voltage capacitors are available commercially. A simple modification converts the divider into a 90° phase shifter.

The originally estimated systematic uncertainty based on the calibration data of the critical passive components was ±14 μrad for the voltage divider mode and ±20 μrad for the phase shifter mode of operation. A part of this paper describes the verification of this estimate by calibrating the device on system basis.

II. OPERATING PRINCIPLES

The voltage divider configuration is illustrated in Fig. 1. The usual operational amplifier circuit is modified by the addition of a controlled source $V_2$ that is phase-locked with the input source $V_1$ and is adjustable in magnitude and phase angle to produce negligible voltage $V_4$ at the junction between the two capacitors. A feedback amplifier with relatively low gain compensates for any drift or imperfect adjustment in the controlled source. A null detector $D$ is utilized to monitor $V_4$ and, hence, the adjustment of the controlled source.

The ratio of voltages on the two capacitors is the negative reciprocal of the capacitance ratio followed by a correction term as shown [1]:

$$V_1/(V_2 + V_3) = -(C_2/C_1) \left[1 - V_4/(V_2 + V_3)\right]$$

The voltages $V_3$ and $V_4$ are related through the gain of the amplifier which, in this case, is $-100$. The controlled source can be adjusted so that $V_3/V_2 < 10^{-4}$. Under such circumstances, the correction term is of the order of 1 ppm or 1 μrad and, thus, negligible. The electronic circuit removes any errors due to the finite gain of the amplifier. Also, since the gain of the amplifier is small, dynamic instabilities in the feedback loop present no problems.

The capacitor $C_1$ is a gas-insulated, completely shielded precision high-voltage capacitor having a phase defect of 5 μrad of smaller. $C_2$ is a solid-dielectric capacitor having a capacitance value that is two to four orders of magnitude larger than $C_1$. Such capacitors have small but significant phase defects, typically in the 50–100 μrad range. Since the three-terminal or transfer capacitance is of interest, the small loss conductance of $C_2$ can be compensated as shown in Fig. 2 with a parallel $T$-network having an effective negative conductance. $R$ is adjusted while the entire network containing $C_2$ is compared in an impedance network containing $C_2$. The impedances are adjusted so that the magnitude of the impedance is close to $C_2$; the phase angle of $C_2$ is accurately obtained through a null detector on system basis.

The common usage of $C_2$ is for high-power applications, where the power factor is of critical importance. The capacitors are available commercially.

In the high-power regime, the phase defects of $C_2$ destroy the feedback amplifier structure. A common practice is to compensate these defects by subtracting a phase angle that is proportional to the magnitude of the error. The authors have seen this procedure in practice and it is important that these phase defects be subtracted at the early stages of the feedback loop.
bridge to a gas-dielectric capacitor. This arrangement provides two capacitive networks, \( C_1 \) and \( C_2 \) in Fig. 1, with negligible phase-angle defect. Availability of two such capacitive networks then becomes the only critical requirements in fabricating a divider with negligible phase defect.

There are usually several possibilities for obtaining a phase-locked source for the realization of \( V_2 \): a tap in the supply transformer, an auxiliary voltage divider, or a separate guard capacitance in the high-voltage capacitor. For this particular instrument the high-voltage capacitor unit has two shielded low-voltage electrodes and, thus, another stable high-voltage capacitor \( C \) besides \( C_1 \). The controlled source is instrumented utilizing this extra capacitor and operational amplifier circuitry as shown schematically in Fig. 3. The adjustment of the controlled source has been done manually, but automatic adjustment is possible, for example, by employing a phase-sensitive detector and gain adjustment by means of a digital controller and multiplying digital-to-analog converters.

A 90° phase shifter for phase-angle calibration of power transducers is implemented in an analogous manner as the voltage divider: instead of \( C_1 \) and \( C \) in Fig. 3, two resistors are used, one of which must have negligible phase defect. The latter requirement is readily achieved with stable miniature resistors installed in a shielded enclosure. The lossless feedback capacitor can be degraded with resistors in parallel as shown in Fig. 4 to yield certain specific power factors: 0.01, 0.02, 0.05, and 0.1, in addition to zero. It should be emphasized that only the input resistor and the feedback capacitor must be precision components with respect to their phase defects. The phase shifter, unlike the voltage divider, is low-voltage instrument. Calibration of a "power transducer" (actually a voltage multiplier) is illustrated in Fig. 4. The phase shifter of Fig. 4 provides negative phantom power to the transducer. Connecting a unity-gain inverting amplifier in cascade with the phase shifter provides positive power. In addition, the combined instrument is useful in self-calibration mode as described later.

III. DESIGN AND CONSTRUCTION DETAILS

Fig. 5 is the circuit diagram of the active divider/phase shifter including the external precision components \( C_1 \), \( C_2 \), and \( C \) that correspond to those in Fig. 3. The amplifier \( A_1 \) is the main amplifier with a gain of 100; \( A_3 \), \( A_4 \), and \( A_5 \) constitute the controlled source; \( A_2 \) is the summing amplifier adding the outputs of the main amplifier and the controlled source and \( A_6 \) and \( A_7 \), together with the capacitors at their inputs, serve for blocking dc offsets that are amplified by \( A_1 \) and \( A_5 \). Some compensation in the frequency response and amplitude limiting had to be introduced at the input to \( A_7 \) to ensure dynamic stability. The voltages \( V_1 \), \( V_2 \), \( V_3 \), and \( V_4 \) correspond to those in Fig. 3.

The amplified summing point voltage can be monitored with a null detector or an oscilloscope either directly at BNC3 or after tuned amplification at BNC4. The summing point voltage is adjusted by means of the variable resistors in the feedback loop of \( A_4 \).
The principal passive precision components are: a dual 100-pF/20-pF gas-dielectric high-voltage capacitor for the input; and three solid-dielectric capacitors, 0.01 μF, 0.1 μF, and 1.0 μF, for the feedback loop. Since the feedback capacitors are not changed in the auxiliary amplifier, its gain is made adjustable over three decades. Of course, another pair of capacitors having the capacitance ratio of 5:1 can be used for input.

In the phase-shifter application, two resistors having the values of 26.5 kΩ and 132.5 kΩ, respectively, are used for the input. The appropriate power factors are obtained by using resistors in the feedback loop as indicated in Fig. 4. The phase angle in the auxiliary amplifier is shifted by means of a decade resistor in the feedback of A3.

The inverter that must be cascaded to the amplifier to generate positive losses is shown in the lower right portion of Fig. 5. Provisions are made to trim the inverter to eliminate both the in-phase and quadrature corrections. The procedure is identical to that described later for the entire active divider and is illustrated in Fig. 6(a).

IV. ACCURACY VERIFICATION

The design philosophy of the active divider and the phase shifter was to ensure their accuracy through the calibration of passive components, and to monitor the performance of the amplifier at its summing point. A system-based verification is still useful in order to detect possible sources of error which might not be obvious from the tests on individual components. Pickup of extraneous signals by the sensitive parts of the active divider, layout of connecting leads, and insidious defects in grounding could produce errors that may not be observed in the passive components.

The calibration and adjustment of the standard capacitors has been discussed previously [1]. In approximately two years since the original calibration, no changes have been observed in the phase angles of capacitors. The limits of phase-angle defect of the critical resistors is estimated from the geometrical considerations of the resistors and their enclosures.
### TABLE I

**Tests of Active Divider**

<table>
<thead>
<tr>
<th>V1 (volts)</th>
<th>Nominal ratio of C1/C2</th>
<th>α (ppm)</th>
<th>β (ppm)</th>
<th>Notes capacitors used*</th>
</tr>
</thead>
<tbody>
<tr>
<td>9</td>
<td>1.0</td>
<td>-14</td>
<td>-6</td>
<td>0.1(N), 0.1(A)</td>
</tr>
<tr>
<td>9</td>
<td>1.0</td>
<td>+8</td>
<td>+2</td>
<td>0.1(A), 0.1(N)</td>
</tr>
<tr>
<td>9</td>
<td>1.0</td>
<td>+5</td>
<td>+2</td>
<td>IVD reversed</td>
</tr>
<tr>
<td>19</td>
<td>0.1</td>
<td>+34</td>
<td>-9.5</td>
<td>0.001(A), 0.1(A)</td>
</tr>
<tr>
<td>175 and 350</td>
<td>0.01</td>
<td>---</td>
<td>+13</td>
<td>0.001(N), 0.1(A)</td>
</tr>
<tr>
<td>175 and 350</td>
<td>0.01</td>
<td>+14</td>
<td>+6</td>
<td>0.001(N), 0.1(A)</td>
</tr>
<tr>
<td>15</td>
<td>0.1</td>
<td>-18</td>
<td>± 5**</td>
<td>0.01(N), 0.1(A)</td>
</tr>
<tr>
<td>15</td>
<td>0.1</td>
<td>-43</td>
<td>± 5</td>
<td>0.01(A), 0.1(A)</td>
</tr>
<tr>
<td>15</td>
<td>0.1</td>
<td>+49</td>
<td>± 5</td>
<td>0.001(A), 0.01(A)</td>
</tr>
<tr>
<td>15</td>
<td>0.1</td>
<td>+21</td>
<td>± 5</td>
<td>0.001(A), 0.01(N)</td>
</tr>
<tr>
<td>15</td>
<td>0.1</td>
<td>+38</td>
<td>±12</td>
<td>0.1(A), 1.0(A)</td>
</tr>
</tbody>
</table>

*The numbers refer to the capacitance values in μF; "N" denotes NBS capacitors; "A" denotes ASEA Electric capacitors.

**For the values preceded by the ± sign, only the magnitude of β was determined.

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**A. Active Divider**

Two circuits were utilized to check the divider at the ratios of 1:1, 1:10, and 1:100. These circuits, Fig. 6, employ as reference dividers a multidecade inductive divider in one case and a passive capacitive divider in the other case. The resultant bridge circuits are balanced for the in-phase component only. During a part of the tests a phase-sensitive detector was used which enabled us to obtain the sign for the quadrature or the phase-angle correction. For some of the tests, only the magnitude of the quadrature correction was measured by using a null detector.

The test results are given in Table I. The voltage ratio of the divider is expressed, using the symbols in Fig. 6, as

\[ V_2/V_1 = -(C_1/C_2) (1 + \alpha + j\beta) \]  (2)

where C1/C2 is the actual capacitance ratio obtained from the calibration of the capacitors, and α and β are the in-phase and quadrature corrections, respectively. In the cases where only the magnitude of β was determined, the value in Table I is preceded by the ± sign.

As noted from the results, in most cases the quadrature error is within 10 ppm as expected from the calibration of the components involved in the circuits of Fig. 6. In the three cases where the correction is near or above 10 ppm, the likely causes of error were identified as extraneous signals. The in-phase corrections are larger, but within 100 ppm. This value is consistent with the expected stabilities of solid-dielectric capacitors. All corrections are within the requirements of the application for which the divider was designed.

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**B. Phase Shifter and Inverter**

The phase shifter, both with and without the inverter, was tested using the two circuits in Fig. 7: against a passive RC network and against a phase-angle standard that is based on digital synthesis technique [7]. The input and output voltages in the circuit of Fig. 7(a) are related by

\[ V_2/V_1 = j(1/\omega R_1 C_2) (1 + \alpha + j\beta). \]  (3)

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![Fig. 6. Calibration of active voltage divider (a) against inductive divider; (b) against passive capacitive divider.](image-url)
TABLE II

TESTS OF PHASE SHIFTER/INVERTER AT ±90°

<table>
<thead>
<tr>
<th>$V_1$ (volts)</th>
<th>Nominal phase angle</th>
<th>$\beta$ (ppm)</th>
<th>Notes components used*</th>
</tr>
</thead>
<tbody>
<tr>
<td>9</td>
<td>+90°</td>
<td>±3**</td>
<td>26.5(A), 0.1(A)</td>
</tr>
<tr>
<td>9</td>
<td>+90°</td>
<td>+2</td>
<td>26.5(A), 0.1(A)</td>
</tr>
<tr>
<td>9</td>
<td>+90°</td>
<td>-5</td>
<td>26.5(A), 0.1(N)</td>
</tr>
<tr>
<td>5</td>
<td>+90°</td>
<td>0</td>
<td>phase standard</td>
</tr>
<tr>
<td>5</td>
<td>-90°</td>
<td>+3</td>
<td>phase standard</td>
</tr>
</tbody>
</table>

*26.5 is the resistance value in kilohms; 0.1 is the capacitance value in μF. “A” denotes ASEA Electric’s components; “N” denotes NBS’ components.

**For the value preceded by the ± sign, only the magnitude of $\beta$ was determined.

TABLE III

TESTS OF PHASE SHIFTER/INVERTER AT POWER FACTORS OF 0.01 TO 0.1

<table>
<thead>
<tr>
<th>$V_1$ (volts)</th>
<th>$R_2$ (megohms)</th>
<th>Generated power factor (G)</th>
<th>Measured power factor (M)</th>
<th>Power factor difference (N-G)</th>
</tr>
</thead>
<tbody>
<tr>
<td>5</td>
<td>2.6338</td>
<td>1.0061 x 10^{-2}</td>
<td>1.0067 x 10^{-2}</td>
<td>6 x 10^{-6}</td>
</tr>
<tr>
<td>5</td>
<td>1.3190</td>
<td>2.0090 x 10^{-2}</td>
<td>2.0091 x 10^{-2}</td>
<td>1 x 10^{-6}</td>
</tr>
<tr>
<td>5</td>
<td>0.52782</td>
<td>5.0141 x 10^{-2}</td>
<td>5.0157 x 10^{-2}</td>
<td>16 x 10^{-6}</td>
</tr>
<tr>
<td>5</td>
<td>0.26308</td>
<td>1.0022 x 10^{-1}</td>
<td>1.0023 x 10^{-1}</td>
<td>10 x 10^{-6}</td>
</tr>
</tbody>
</table>

$C_2 = 1.00103 \, \mu F$

Only the quadrature correction is of interest here. When the instrument is serving as a phase shifter, as illustrated in Fig. 4, the magnitudes of the input and output voltages are measured independently.

For the test points of ±90°, the results are presented in Table II. In the first measurement reported in Table II, only the magnitude of $\beta$ was determined, hence the ± sign. For the phase angles corresponding to specific power factors of 0.01, 0.02, 0.05, and 0.1, the test data are given in Table III.

The quadrature corrections for all tests at ±90° are well within 10 ppm. At specific power factors the predicted and measured power factor values agree within ±0.1 percent of the nominal power factor. In fact the largest phase angle correction is 16 μrad in the total phase shift of 50 000 μrad.

C. Self-Calibration of Phase Shifter and Power Transducer

Phase-angle measurements are ratio determinations. Hence, self-contained methods, especially at cardinal points of 180° and ±90° can be implemented without recourse to higher echelon standards. Using the circuit of...
Fig. 8. Self-calibration of phase shifter and power transducer at ±90° points. (The input terminals of the transducer, $T_1$ and $T_2$, are alternatively connected to $V_1$, $V_2$, and $V_3$. A sequence of measurements as discussed in the text yields correction for the phase shifter and the transducer. The inverter is assumed to be perfect.)

Fig. 8, self-contained calibration of the phase shifter and the power transducer is possible at ±90°. If the power transducer is linear—that is, the phase angle corrections to the phase-angle corrections of the phase shifter and the power transducer, respectively.

Electronic transducers may not have identical corrections at leading and lagging power factors are identical—only two measurements are required. For the first the connection is as in Fig. 8; for the second, the connections to the power transducer are reversed. The sum of the two power readings and the difference between them are proportional to the phase-angle corrections of the phase shifter and the power transducer, respectively.

Equations (4) and (7) are solved for $\beta$ and $a$; and (5) and (6) yield a value for $b$ and a redundant value for $\beta$. Such a test was performed, but with less than fully satisfactory results. Two types of difficulties were encountered: 1) the available power transducer, while within its specifications and quite satisfactory for its intended use, had peak-to-peak noise of about 20 $\mu$rad; and 2) for the particular experimental setup used, the phase shifter appeared to have a quadrature bias of 10 ppm. The definitive verifications of the self-testing procedure has yet to be carried out, nevertheless the theory appears sound.

The instrument was designed and built for the specific purpose of calibrating voltage dividers and power transducers as used in the test equipment for measuring transformer losses. In this application the primary concern is to achieve very low phase-angle uncertainty.

Experience within the past year has shown that the ratio stability can be maintained within about 100 ppm if some temperature control (±1°C) is available in the laboratory. Thus the divider can be used as a standard to calibrate metering-type voltage transformers and other moderately accurate dividers. The phase shifter, if combined with a stable transconductance amplifier [8], could serve as a quadrature volt-ampere source, for example to calibrate meters having standard inputs of 120 V and 5 A. The phase shifter could also be used to suppress the quadrature signal to a low-power-factor power transducer, thus increasing the power factor as detected by the transducer. Direct measurement of loads having exceptionally low power factors, such as shunt reactor, power capacitors, and cables, might be feasible with moderately accurate power transducers.

VI. CONCLUSIONS

The relatively simple instrument as described has proven to be a very versatile tool in calibrating and aligning, on a routine basis, test equipment that is used to measure power losses at low power factors. Much attention has been paid to have a self-contained system that would require minimal calibrations from outside. The results presented in this paper demonstrate that predictions of system performance given earlier [1] are valid.

While the circuit is deceptively simple, work at the accuracy levels involving microradians is never trivial. Effects that are negligible at 1-percent level could completely invalidate the results at the 10-ppm level. Full appreciation of the operation of the system and awareness of sources of error is just as essential as any system of comparable accuracy.

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