Reconfigurable RF Components for Multifunction RF Systems

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Abstract — The proliferation of the wireless market has driven the demand for RF systems to dramatically increase functionality, frequency coverage, bandwidth, and efficiency; while simultaneously reducing power consumption and die size to save power and cost. For example, the highly complex RF systems in handset technology have seen a dramatic increase in frequency, operating bands, and complexity of signals with each generation (from 1G to 4G). The next generation (5G) will continue the trend to extend the frequency into mm-waves due primarily to spectrum congestion, bandwidth, and data rate. DoD is facing comparable challenges in terms of increasing the frequency of operation, bandwidth, and functionality while maintaining cost and accelerating technology insertion (or time to market for commercial). This paper reports on the impact of these new challenges for RF components and discusses solutions under investigation.

Index Terms — Reconfigurable architectures, Tunable circuits and devices, Cognitive radio, Scalability, Switched circuits

I. INTRODUCTION

Wireless System and sensor technology requirements increase dramatically with each generation upgrade for both commercial and military applications. New system demands for increasing functionalities (multi-mode and multi-standards) along with backward compatibility while simultaneously reducing system cost, size, weight, and power resulting in tremendous challenges for RF designers.

Traditional RF microsystems use fixed-frequency transceivers optimized for performance in a particular fixed band of interest. With the rapid increase in electromagnetic spectrum (EMS) utilization, these fixed frequency systems are increasingly vulnerable to in-band and near band interference. The trend in commercial wireless is to design highly optimized compact chipsets for each operating band with each generation seeing a dramatic increase in frequency, operating bands, and RF performance. The trend for military systems is to push the digital interface closer to the RF front-end and perform programmable and reconfigurable functions in the digital domain where programmable signal processing can be accomplished with CMOS-based digital signal processing (DSP). This approach works best if a wide instantaneous bandwidth is digitized, however maintaining high dynamic range and efficiency over broad instantaneous bandwidths is challenging. Performance of the overall transceiver and optimization of the DSP blocks can be enhanced by a reconfigurable RF front end. However, this type of reconfiguration has typically come with unacceptable performance penalties in the form of switching loss, reduced gain, reduced efficiency and increased spurious transmissions.

Fig. 1 shows a notional reconfigurable transceiver architecture. Similar to a traditional RF frontend (RFFE), it consists of a transmitter and a receiver to enable radar or communication function. The RFFE is further divided into transmit / receive (T/R) module and receiver/exciter module. Critical blocks for the T/R module are the power amplifier (PA), low noise amplifier, RF switch, and phase shifter (for phased-array technology). The receiver/exciter module consists primarily of mixers, filters, and variable gain buffer amplifiers. Salient features of the reconfigurable architecture include the ability to 1) adjust frequency plan, both RF and IF, 2) adaptively filtering to enhance signals of interest and reject unwanted interfering signals, 3) adjust gain to optimize system dynamic range and efficiency, 4) adjust instantaneous bandwidth.

In order to realize the architecture depicted in Fig. 1, several reconfigurable components and enabling technologies are needed. Fortunately, there have been recent advances in key RF blocks and enabling technologies to make a reconfigurable RF system. This paper examines drivers to explore innovative solutions to meet the demands of future RF systems by implementing reconfigurable RF components. This paper first examines the fundamental building block, the RF switch, to enable...
reconfigurable RF components. A comparison of critical switch parameters is assessed for various RF switch technologies: solid state switches, MEMS-based switches, and phase change material (PCM) switches. The next section surveys PA techniques allowing multi-mode operation. The final section describes reconfigurable receiver/exciter architectures for efficient frequency conversion.

II. RF SWITCH TECHNOLOGIES

For reconfigurable RF components, a low loss, high frequency, and high power handling RF switch is a fundamental building block to tune the input/output matching networks for adjusting impedance states to match the active devices. RF switch technology is divided into three categories: solid state switches, microwave electromechanical switches (MEMS), and phase change material (PCM) switches. This section examines these switch technologies with respect to (1) insertion loss, (2) isolation, (3) power handling, and (4) switching speed.

A. Solid State RF Switches

There are a variety of solid state RF switches available on the market. These range from the low power silicon CMOS/SiGe to the high power handling in GaN technology. A salient feature of the solid state switches is the fast switching speed, typically in 10s of nano-seconds. The main differences between these switches are insertion loss and power handling. Gallium-nitride (GaN) high-electron mobility transistors (HEMT) switches are an attractive technology because they can potentially be monolithically integrated with state-of-the-art GaN HEMT microwave power amplifiers. Broadband (DC to 18 GHz) GaN HEMT switches are commercially available with insertion loss below 1.5 dB, 30 dB isolation, 10 W power handling, and 35 ns switching speed. Silicon and GaAs PIN (p-type intrinsic n-type) diodes have been a reliable, mature high-performance RF switching technology for many years. PIN switches dissipate significantly more power than FET switches. Broadband (0.050 – 26.5GHz) silicon PIN switches are commercially available with insertion loss below 1 dB, isolation greater than 50 dB, 30 dBm power handling, linearity greater than 40 dBm, while dissipating approximately 50 mW, in both the on-state and off-state for a series-shunt configuration. Broadband (0.2 – 18 GHz) GaAs PIN switches are commercially available with insertion loss below 0.5 dB, isolation greater than 35 dB, while dissipating approximately 20 mW, in both the on-state and off-state for a series-shunt configuration. Silicon-on-insulator (SOI) field effect transistors (FETs) have increased in RF performance over the years. While SOI switches still cannot realistically compete with GaN in the areas of bandwidth and power handling; they have significantly better insertion loss than CMOS switches and have replaced GaAs pHEMT in many wireless front-end applications requiring large number of RF switch ports.

Another GaN device, the super lattice castellated gate FET (SLCfET) [1], is a variant of GaN HEMT technology currently under research and development. Preliminary laboratory results have shown excellent RF performance, potentially capable out-performing all other types of solid-state switches. The SLCfET cannot be easily monolithically integrated with a conventional GaN HEMT microwave amplifier process due to different material profile. Manufacturability and lifetime of SLCfET switches have not yet been established.

B. Phase-Change Switches

Another type of solid-state switch made from thin-film germanium-telluride (GeTe) is in the early stages of research and development at a handful of laboratories. GeTe switches operate by changing between polycrystalline and amorphous solid phases through a pulsed heating process. Although good RF performance has been demonstrated, currently the lifetimes of these devices are well short of what is required for a T/R switch. Switching times are in the 100-200 ns range.

C. Microwave Electromechanical Switches

RF microelectromechanical systems (MEMS) switches have demonstrated exceptional RF performance but suffer from slow switching speeds (5-10 microseconds) and present challenges in the areas of packaging, integration, and manufacturing. Although many groups have demonstrated high-performance RF MEMS switches, there are currently no commercial off-the-shelf sources for these devices.

III. RECONFIGURABLE POWER AMPLIFIERS

Multi-function, multi-mode T/R modules require PAs able to operate on signals both with different waveform types (instantaneous bandwidth, peak-to-average power ratio, etc) and at different frequencies. While efficiency-enhancement techniques such as Doherty and envelope tracking (ET) provide improved efficiency for high-PAPR signals, and wideband PAs are capable of covering multiple bands, developing a PA solution that is simultaneously linear, efficient, and wideband remains an active challenge. In this section, we summarize a sample of approaches based on reconfigurable structures to beat the linearity-efficiency-bandwidth tradeoff of conventional PAs.

A. Waveform-Agile PAs

Waveform-agile PAs allow the active devices to operate efficiently at varying power levels or signal PAPRs, maintaining favorable conditions through techniques
including adaptive bias [2], reconfigurable output [3] or interstage [4] matching networks, or combinations of these techniques [5]. Architectures such as Doherty and ET PAs can also support waveform-agility, particularly when made reconfigurable through bias control [2,6].

Although primarily used in CMOS PAs [4,5], power scaling through reconfigurable device periphery has been demonstrated at AFRL in GaN. Theoretically, increasing the device periphery by 2x will increase output power by 3 dB, and when combined with drain supply variation, a total of 10 dB power scaling is achievable. Analysis shows that placing the RF switch at the gate terminal results in reduced loss compared to the drain, largely because the switch insertion loss is on the input side of the device. A reconfigurable transistor core was fabricated in a 0.25µm GaN foundry process and measured with device on/off states and power supply scaling as summarized in Table 1.

### Table 1. Load Pull measured data of the reconfigurable transistor core at 3GHz.

<table>
<thead>
<tr>
<th>Pin (dBm)</th>
<th>VDD (V)</th>
<th>Pout (dBm)</th>
<th>PAE (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>One device on</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>21.7</td>
<td>10</td>
<td>27.2</td>
<td>49</td>
</tr>
<tr>
<td>21.7</td>
<td>20</td>
<td>29.7</td>
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<tr>
<td>21.7</td>
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<tr>
<td>24.6</td>
<td>30</td>
<td>34.2</td>
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### B. Frequency-Reconfigurable PAs

Traditionally, multiband RF frontends have been implemented using either PAs with fixed matching networks covering a broad frequency, or by using multiple individual narrowband PAs. An ideal solution for a true multi-function, multi-mode T/R module, however, would use a single PA operating at an arbitrary RF carrier.

Of particular interest are those techniques that incorporate waveform-agile efficiency enhancement architectures in a multi-band PA. Techniques incorporating power combining, such as Doherty and outphasing PAs, can be difficult to extend to wideband operation while maintaining loading conditions on the device. Nonetheless, significant effort has been made to develop wideband Doherty techniques. One example with both extended bandwidth and reconfigurable efficiency is found in [6], in which a modified Doherty structure is reconfigured using the relative phase of its RF inputs (rather than tunable reactive elements) and operates from 1.5 to 2.5 GHz with over 49% drain efficiency.

Alternatively, direct dynamic load modulation (DLM) provides load modulation as in a Doherty amplifier, but by directly controlling the load impedance through a tunable OMN. Although it lacks the λ/4 impedance inverter that limits conventional Doherty bandwidth, DLM is typically designed for single-band operation due to the challenges of the DLM matching. Despite this, an octave-bandwidth adaptive PA based on a commercial GaN HEMT and diode varactor tuning has been demonstrated with 64%-79% peak-power efficiency over 1-1.9 GHz [7].

Limitations in power handling of varactors and RF switches can present limitations in tunable networks for high-power applications. An alternative technique that employs phase control rather than tuning of circuit elements has recently been reported [8]. This load modulated balanced amplifier approach uses active drive of the isolation port to control the load impedance trajectory for the branch PAs over a broad frequency range. The approach has been shown over 800-2000 MHz with >70% efficiency.

### III. RECEIVER/EXCITER ARCHITECTURES

At the system level, RF switches may be employed to enable coarse-grained, mid-grained, or fine-grained reconfigurable RF components and systems as depicted in Fig 2. Coarse grain architecture employs RF switches to select different components or channels such as fixed frequency/function high band, mid-band, or low band RF blocks. Medium grain architectures can tune the input/output matching networks for adjusting impedance states to active devices, altering the transfer function of specific devices. Fine grain architecture consists of a matrix of RF switches to connect both active and passive elements to form an RF function.

Fig 2: Granularity of reconfigurable architectures

An example of a coarse grain reconfigurable component is the ubiquitous switched filter bank, where N unique filters are laid out in parallel with single pole N throw switches on either side to create an N-way switched filter bank. Similarly, the entire RF chain could be constructed from switched fixed function components/blocks.
However, this implementation leads to size and cost that increases linearly with number of possible configurations. Fine grain architectures are not yet practical solution due to unacceptable performance penalties associated with reconfigurable transistor cells with a large numbers of cells.

Reconfigurable PAs discussed in this paper are examples of a medium grain reconfigurable component. Another example are tunable filters based on tuned cavities or compact capacitively coupled resonators [9,10]. These filters can be made more compact, and with greater tuning fidelity than coarse grain switched filter banks.

The heart of a medium grain reconfigurable architecture are reconfigurable agile core components depicted in Fig 2. These “supercomponents” can transform operation from variable gain amplifier, to mixer to through-line depending on bias and drive settings. Wideband programmable linearity, gain and power consumption are key attributes to make the supercomponent viable. SiGe is a leading candidate technology to make supercomponents with InP offering potentially lower NF and greater linearity at the expense for greater cost and lower levels of integration.

The MATRICs chip is an example of a medium grain transceiver built by integrating medium grain configurable RF/baseband block and microwave blocks [11]. This architecture is enabled by advances in SiGe switch technology.

Another key attribute of reconfigurable RF systems is granularity of reconfiguration. Options include: Factory reconfigurable: where connections and bias/passive settings are tailored for each application but cannot be changed, resulting in lower NRE and design time than full custom solutions. Reconfigure at system start: where a configuration is loaded at system startup but cannot be changed while running. Reconfigure mode-to-mode: where the user can initiated configuration changes (open loop), Reconfigure on-the-fly to predefined states: where the system can sense and reconfigure autonomously to a limited set of predefined states (closed loop), or Reconfigure on-the-fly, where the system can sense and reconfigure autonomously to any legal state (cognitive closed loop). The time constant associated with reconfiguring and settling varies dramatically for each level of granularity from weeks for factory reconfiguring to seconds for reconfigure at startup to msec for open loop and usec for closed loop reconfiguration. The options discussed in this paper are most suited to reconfigure mode-to-mode. On the fly reconfiguration requires decision logic embedded with the RF control which is not addressed in this paper.

Building on these ideas, the Air Force Research Laboratory has initiated a research effort to demonstrate leap-ahead agile RF front-end systems with multi-function transmit / receive technology by employing reconfigurable RF & mixed-signal components [12].

IV. CONCLUSION

In this paper, we discuss key attributes of a reconfigurable RF system and highlight current state of the art for critical reconfigurable components including RF switches and reconfigurable power amplifiers. We also introduce the concept of coarse, medium and fine grain reconfigurable systems and granularity of reconfiguration.

REFERENCES