Abstract — The DARPA Microsystems Technology Office is developing revolutionary materials, devices, and integration techniques for meeting the RF integrated circuit performance requirements for advanced modern RF systems. DARPA is enabling these systems through systematic development of materials and devices, circuits, and integration technologies for compound semiconductors. The DARPA Nitride Electronic Next-Generation Technology (NEXT) program is developing high performance nitride transistors for high-speed RF, analog and mixed signal electronics, thus overcoming the Johnson figure of merit limits to achieving simultaneous high-speed operation and high breakdown voltage. The DARPA Microscale Power Conversion (MPC) program is developing nitride-based technology to enable dynamic envelope-tracking power conversion embedded in RF radiating elements. The DARPA Diverse Accessible Heterogeneous Integration (DAHI) program is developing transistor-scale heterogeneous integration processes to intimately combine advanced compound semiconductor (CS) devices, as well as other emerging materials and devices, with high-density silicon CMOS technology. Taken together, these programs are addressing many of the critical challenges for next-generation RF modules and seek to revolutionize DoD capabilities in this area.

Modern RF systems are under consistent pressure to make use of the RF spectrum in increasing sophisticated ways. As the RF spectrum becomes increasingly congested, RF systems must contend with increased interference while achieving frequency agility and spectral efficiency and simultaneously achieving increased data rates. Additionally, as mobile technology becomes more pervasive in both commercial and DoD technology, novel RF systems must work within increasingly limited power budgets. Furthermore, there is a constant push to achieve reduced size and weight for RF systems of all size scales.

Such system requirements create concomitant challenges for RF integrated circuits for which compound semiconductors provide a potential solution. The compound semiconductor (CS) electronics industry has a long history of driving advancements in RF/mixed signal systems. The success of CS materials in these systems is due in large part to the many superior properties of these materials relative to silicon. For example, high electron mobility and peak velocity of InP-based material systems have resulted in transistors with $f_{\text{max}}$ above $1\text{THz}$ [1] as well as ultra-high-speed mixed-signal circuits (see, for example, [2]). The wide energy bandgap of GaN has enabled large voltage swings as well as high breakdown voltage RF power devices [3]. Excellent thermal conductivity of SiC also makes tens of kilowatt-level power switches possible [4]. Additionally, on-chip high-Q micro-electromechanical resonators and switches in various materials, such as AlN, have been demonstrated that potentially can be used for clock references and frequency selective filters [5].

The advantages of CS materials are illustrated in Table 1 below. As indicated by Table 1, compound semiconductors exhibit many superior materials properties relative to silicon technology. However, the modest complexity of CS circuits relative to silicon circuits has limited their deployment in many applications. In particular, GaN is a material system with an unparalleled combination of high breakdown field, band gap, and thermal conductivity, but with limited integration complexity to date. Overall, it is clear that no materials system excels for all metrics highlighting the benefit to integration.

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<table>
<thead>
<tr>
<th>Parameter</th>
<th>Why?</th>
<th>Unit</th>
<th>Si</th>
<th>GaAs</th>
<th>ABCS 1</th>
<th>InP 2</th>
<th>GaN 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Electron Mobility</td>
<td>Carrier velocity</td>
<td>$10^4$ cm$^2$/V·s</td>
<td>1.4</td>
<td>8.5</td>
<td>40</td>
<td>12</td>
<td>&lt;1</td>
</tr>
<tr>
<td>$v_{\text{peak}}$</td>
<td>Transit time</td>
<td>$10^6$ cm/s</td>
<td>1</td>
<td>2</td>
<td>8</td>
<td>2.5</td>
<td>2.5</td>
</tr>
<tr>
<td>$E_{\text{ac}}$</td>
<td>Voltage swing</td>
<td>$10^6$ V/cm</td>
<td>5.7</td>
<td>6.4</td>
<td>0.4</td>
<td>4</td>
<td>40</td>
</tr>
<tr>
<td>$E_{\text{g}}$</td>
<td>Charge density</td>
<td>eV</td>
<td>1.12</td>
<td>1.42</td>
<td>0.35</td>
<td>0.74</td>
<td>3.4</td>
</tr>
<tr>
<td>$K$</td>
<td>Heat removal</td>
<td>W/cm$^2$K</td>
<td>1.3</td>
<td>0.5</td>
<td>0.27</td>
<td>0.05</td>
<td>2.9</td>
</tr>
<tr>
<td>Maturity</td>
<td>Circuit Complexity</td>
<td>Excellent</td>
<td>Good</td>
<td>Limited</td>
<td>Ok</td>
<td>Limited</td>
<td></td>
</tr>
</tbody>
</table>

Table 1. Material properties and circuit maturity of various semiconductor technologies. Footnotes: 1 – InAs channel, 2 – InGaAs channel, 3 – SiC substrate.

In order to address the challenges of next-generation RF system requirements, substantial innovation in compound semiconductor materials, devices, circuits, and integration technologies is required. The DARPA Microsystems Technology Office has invested in a number of programs which seek to provide this innovation. The DARPA Nitride Electronic Next-Generation Technology (NEXT) program [6] is
developing high performance nitride transistors for high-speed RF, analog and mixed signal electronics, thus overcoming the Johnson figure of merit limit to achieving simultaneous high-speed operation and high breakdown voltage. The DARPA Microscale Power Conversion (MPC) program [7] is developing nitride-based technology to enable dynamic envelope-tracking power conversion embedded in RF radiating elements. The DARPA Diverse Accessible Heterogeneous Integration (DAHI) program [8] is developing transistor-scale heterogeneous integration processes to intimately combine advanced compound semiconductor (CS) devices, as well as other emerging materials and devices, with high-density silicon CMOS technology. This paper reviews the latest technical progress of these three DARPA programs.

II. DARPA NEXT PROGRAM

The DARPA NEXT program has made significant strides in enhancing the capabilities of GaN transistor technology to more fully realize GaN’s potential for high speed-high breakdown devices, as indicated by its superior Johnson figure of merit [10]. As reported previously [6], NEXT has aggressively pushed the operating speed of GaN HEMTs by simultaneously minimizing carrier transit time, maximizing electron density, reducing access resistances, and optimizing parasitic capacitances with novel device structures.

The DARPA NEXT program goals focus on GaN enhancement-mode and depletion-mode HEMT performance, yield, uniformity, and reliability. These are shown in Figure 1, with the check marks indicating metrics that have been met by the program. This table indicates the considerable success that the DARPA NEXT performers have achieved in relation to the program goals.

<table>
<thead>
<tr>
<th>Metric</th>
<th>Unit</th>
<th>Today</th>
<th>Phase I</th>
<th>Phase II</th>
<th>Phase III</th>
</tr>
</thead>
<tbody>
<tr>
<td>D-mode f_max</td>
<td>GHz</td>
<td>150</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
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<tr>
<td>D-mode f_T</td>
<td>GHz</td>
<td>200</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
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<tr>
<td>D-mode Johnson PDM</td>
<td>THz-V</td>
<td></td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>E-mode f_max</td>
<td>GHz</td>
<td>65</td>
<td>✔</td>
<td>✔</td>
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</tr>
<tr>
<td>E-mode f_T</td>
<td>GHz</td>
<td>100</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>E-mode Johnson PDM</td>
<td>THz-V</td>
<td></td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
</tbody>
</table>

Figure 1. DARPA NEXT program goals. Check marks indicate goals which have been met.

The current status of HEMT operating speed in the DARPA NEXT program is shown in Figure 2. Aggressive device scaling has resulted in enhancement- and depletion-mode GaN HEMTs with dramatically improved unity-gain cutoff frequency (f_T) and maximum oscillation frequency (f_max) as compared to the state-of-the-art prior to the program. Additionally, NEXT has aggressively improved device yield and circuit complexity, achieving a 501-stage ring oscillator using both enhancement-mode and depletion-mode HEMTs on a single substrate.

III. DARPA MPC PROGRAM

The DARPA MPC program has focused on leveraging recent progress in GaN transistor technology to develop GaN MMIC-based RF power amplifiers which utilize envelope-tracking to achieve very high efficiency at X-band frequencies. As reported previously [7], the program envisions co-designing a MMIC power amplifier with a dynamic voltage power supply modulator consisting of a very fast GaN power switch.

MPC program participants have made substantial progress in advancing the state-of-the-art in envelope-tracking power supply technology. Figure 3 plots power-added efficiency versus operating frequency for state-of-the-art power amplifiers. Results from the MPC program show the efficiency increase from 20% to over 50% at 10GHz. Similarly, Figure 4, which plots efficiency versus envelope bandwidth, shows that the MPC program achieved a 500MHz RF envelope bandwidth with an efficiency of 40% or greater, which is more than an order of magnitude increase over the state of the art (20MHz).
Figure 3. Plot of power-added efficiency versus output frequency for state-of-the-art power amplifiers.

Figure 4. Plot of power-added efficiency versus RF envelope bandwidth for state-of-the-art envelope-tracking power amplifiers.

IV. DARPA DAHI PROGRAM

The preceding sections have highlighted many of the advantages of CS materials for RF modules. However, despite the advantages of CS materials, Si CMOS-based technologies have increasingly been employed in high-performance RF/mixed signal systems. These technologies have leveraged the enormous investments in digital CMOS device scaling and process development to achieve tremendous levels of complexity and integration, while also demonstrating far higher levels of yield and manufacturability than any CS technology. The integration density of Si-based technologies has enabled novel on-chip digital correction and linearization techniques (for example, [9]), producing excellent RF and mixed-signal circuit performance despite the limitations of silicon’s material properties.

Such correction techniques have the potential to produce dramatic RF and mixed-signal performance improvements in CS electronics as well; however, CS technologies lack the integration density and yield to implement these circuit concepts. Given these trends, it is our view that the future of high-performance RF and mixed-signal electronics lies in the integration of CS materials with silicon technology in a way that will allow the advantages of the two technology types to be optimally combined.

Figure 5 illustrates the potential impact of heterogeneous integration in RF/mixed signal systems, using a representative transceiver as an example. Essentially all major components in a typical transceiver can potentially benefit from the reduced parasitics of heterogeneous integration, utilizing the benefits of high-performance CS materials with the control and calibration capabilities of Si CMOS.

To that end, the DARPA DAHI program is developing transistor-scale heterogeneous integration processes to intimately combine advanced compound semiconductor (CS) devices, as well as other emerging materials and devices, with high-density silicon CMOS technology. The ultimate goal of DAHI is to establish a manufacturable, accessible foundry technology for the monolithic heterogeneous co-integration of diverse (e.g., electronic, photonic, MEMS) devices, and complex silicon-enabled architectures, on a common substrate platform for defense and commercial users. This capability will not only have significant impacts on the performance of both military and commercial microsystems, but it also represents a new paradigm for the CS electronics community.

One element of DAHI, the Compound Semiconductor Materials on Silicon (COSMOS) thrust, has demonstrated three different approaches (shown in Figure 3) to achieving InP BiCMOS integrated circuit technology featuring InP HBTs and deep submicron Si CMOS [12][13][14] for RF and mixed signal circuits.

DAHI/COSMOS performers have demonstrated complex heterogeneously integrated mixed-signal circuit designs, including digital-to-analog converters (DACs) with unprecedented SFDR performance in the GHz output frequency regime [15] as well as revolutionary ultra-wide band ADCs. The ADC design employs a time-interleaved architecture utilizing InP
HBTs for track-and-hold circuitry and 130nm Si CMOS to provide the sub-ADCs and the circuitry required for complex time-interleaving. This ADC represents the most complex heterogeneously integrated circuit to date, with ~1000 InP HBTs, ~16,000 Si HBTs, and 2500 Si MOSFETs, with more than 1800 heterogeneous interconnects between the InP HBT chiplet and the Si base chip. This approach has resulted in an ultra-wide band ADC with state-of-the-art signal-to-noise-and-distortion ratio (SINAD) of over 30dB across frequency ranges of 2.75-8.75GHz and 14.25-20.25GHz. This performance level is on par with the most advanced ADCs currently demonstrated [16], which required the use of 32nm CMOS technology (as compared to the 130nm CMOS used by the DAHI/COSMOS ADC). It is expected that the combination of InP HBTs heterogeneously integrated with an advanced CMOS node would enable dramatically improved SINAD at higher frequencies, far in excess of the current state-of-the-art. DAHI performers have also demonstrated the world’s first GaN + CMOS RF amplifier using monolithic heterogenous integration of GaN HEMTs with Si pMOS gate bias control [17].

Recently, a new DAHI Foundry Technology thrust was initiated [18] to advance the diversity of heterogeneous device and materials available in a silicon-based platform. This foundry will seek to include a wider array of materials and devices (including, GaN and MEMS technologies) with complex silicon-enabled (e.g. CMOS) architectures and thermal management structures on a common silicon substrate platform. The goal of the DAHI Foundry Technology thrust is to develop a mature, reliable heterogeneous integration technology in a cost-effective foundry.

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