Millimeter-sized Graphene Quantum Hall Devices for Resistance Standards

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Abstract — We present a repeatable process to produce millimeter-sized graphene device for quantum Hall resistance (QHR) standard. Our large-area, homogeneous monolayer graphene is grown on silicon carbide (SiC) substrate at high temperature by an optimized epitaxial method. Well-developed quantum Hall plateaus have been observed in devices fabricated from samples annealed at 1900 °C by a clean lithography process. Metrological accuracy of better than 5 × 10−9 has been observed in a 27 mm2, octagonal device at a record current level of 0.72 mA for temperatures between 1.6 K and 3.1 K.

Index Terms — Epitaxial, FTG, homogeneous graphene, millimeter-sized, quantum Hall effect, Raman microscopy, uniform carrier density.

I. INTRODUCTION

The quantum Hall effect (QHE) provides a universal, stable, and reproducible electrical resistance standard that is determined by two fundamental physical constants: Planck constant $h$ and elementary charge $e$. Before the discovery of graphene, the universality of QHE has been implemented in metal-oxide semiconductor field-effect transistors (MOSFET) and GaAs heterostructures. In these devices, realization of metrology accuracy at the level of few parts per billion (ppb) requires magnetic field on the order of 10 T and low temperature in the range of 0.5 K - 1.5 K, with the measurement current at the level of tens μA [1].

Graphene is a natural 2D electronic system with charge carriers mimicking the behavior of Dirac electrons [2]. This unique electronic property leads to an anomalous QHE with a sequence of QHR plateaus as $R_H(i) = h/(e^2)\times 4(i+1/2)$ (i=1,2,…) . The plateau with the filling factor of $\nu = 4(i+1/2) = 2$ is particularly robust and of great interest to metrology measurement. Recently, the quantization accuracy of few ppb has been reported in several graphene Hall bar devices [3]-[5]. In the exfoliated graphene device (30 μm wide), the measurement current for ~5 ppb is limited to ~10 μA at 18 T and 60 mK. In the epitaxial graphene Hall bar with similar width (30 μm), ppb-level quantization has been demonstrated with a measurement current of 60 μA, in a magnetic field of 14 T, and with temperature up to 15 K. The larger Hall bar (100 μm wide) made from CVD graphene on SiC substrate maintains the accuracy of 1 ppb with 500 μA at 1.3 K and in a magnetic field of 8 T. Although the quantization of Hall resistance has negligible dependence on the Hall bar size [6], larger samples can sustain a higher measurement current which ensures a large signal-to-noise ratio. Other key features to achieve high precision measurement include low contact resistance and homogeneous carrier density in the channel.

We demonstrate that high performance graphene devices can be repeatedly produced using homogeneous epitaxial graphene grown on SiC by an optimized method. The homogeneity is validated with AFM, optical microscopy, Raman microscopy, and magnetotransport. The QHE measurements on a very large octagonal graphene device (27 mm2) show metrological accuracy of better than 5 × 10−9 at 9 T for temperatures between 1.6 K and 3.1 K, with a measurement current of 0.72 mA.

II. EPITAXIAL GRAPHENE GROWTH

Our samples are square substrates (7.6 mm × 7.6 mm) diced from 4H-SiC(0001) wafers that have very low nominal off-set angle relative to the basal plane. Graphene are grown at high temperature using the face-to-graphite (FTG) technique [7] in Ar background gas. The samples has been treated with forming gas (4 % H2, 96 % Ar) while heating the furnace to 1050 °C with 10 to 20 minutes dwell time. We optimize our process according to feedback from magnetotransport measurements and surface characterization including optical microscopy and atomic force microscopy.

Fig. 1 Raman spectra of a line scan from the edge of a FTG sample (d = 0) towards the center. Upper Inset: Optical image showing the line...
Raman microscopy confirms that thick graphene layers only occur near the sample edge. Figure 1 shows the Raman line spectra collected from a FTG sample processed at 1950 °C for 1800 s. Data were collected by scanning from the edge inward, along a line as shown in the upper inset of Fig. 1. The intensity of the graphitic Raman features, both G band (≈ 1600 cm⁻¹) and the G' band (≈ 2700 cm⁻¹), attenuates rapidly with increasing distance from the sample edge. At interior distances \( d \approx 300 \mu m \), the G' peak takes on the shape of a single Lorentzian with a full width at half maximum of \( \approx 40 \text{ cm}^{-1} \), which is the fingerprint for monolayer graphene.

III. MILLIMETER-SIZED GRAPHENE QHE DEVICES

We fabricated five millimeter-sized graphene devices from three FTG samples annealed at 1900 °C. Fig. 2(a) shows the layout of three large Hall bars patterned on sample 1 (S1), which was annealed for 206 s. Fig. 2(b) shows the layout of the 27 mm² octagonal devices fabricated on sample 2 (S2) and sample 3 (S3), which were annealed for 116 s and 210 s, respectively.

![Figure 2](image-url)

Figure 2. Layout and magnetotransport measurements of millimeter-sized graphene devices. (a) Layout of three large Hall bars (0.8 mm × 5.6 mm) on S1. (b) Layout of the large octagonal device (5.6 mm long and 5.6 mm wide) on S2 and S3. (c) Low temperature AC magnetotransport of S2 with measurement current of 1 μA. (d) Carrier density and mobility of S1-S3 at \( T = 1.5 \text{ K} \).

Well-developed \( \nu = 2 \) QHR plateau has been observed in all five devices, similar to Fig. 2(c). The carrier density and mobility of the six regions in S1 (R1-R6) as well as S2 and S3 are listed in Fig. 2(d). The carrier density of S1 has a narrow distribution \( n = (2.9 \pm 0.5) \times 10^{11} \text{ cm}^{-2} \) associated with a high average mobility \( \mu \approx 4200 \text{ cm}^2/\text{Vs} \). Metrology accuracy of better than 5 ppb is measured in S3 at 9 T for temperatures between 1.6 K and 3.1 K, with a current of 0.72 mA.

VI. CONCLUSION

We report a high-yield process to produce millimeter-sized graphene devices suitable for metrology measurements, with low contact resistance and uniform carrier density. This process includes a repeatable synthesis method to grow homogeneous graphene on SiC, and clean fabrication techniques [8] for devices with low contact resistance and low carrier density. Our synthesis method is scalable to produce wafer-size, homogeneous graphene not only for metrology, but also desired by future electronics.

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REFERENCES